Fig. 1

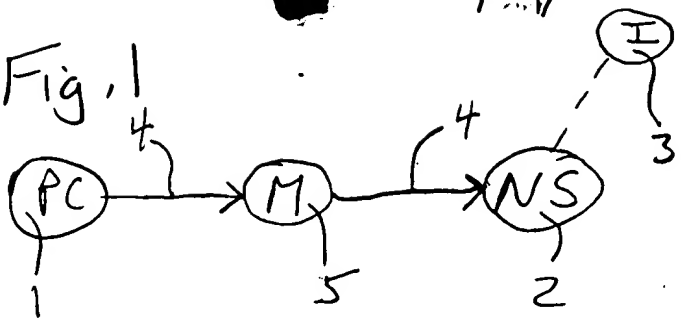


Fig. 2

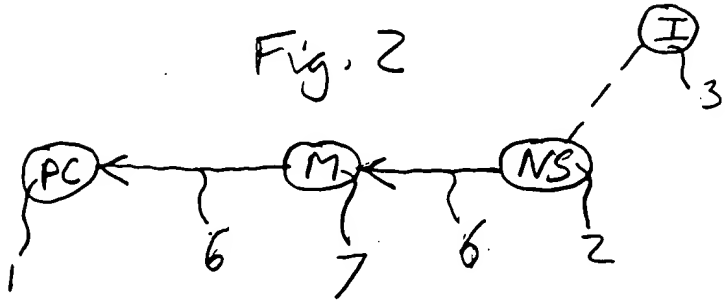


Fig. 3

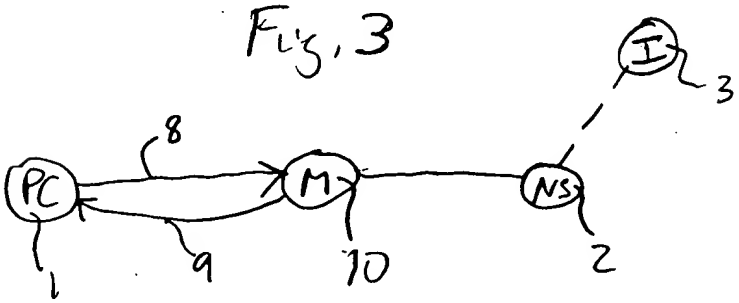


Fig. 4A

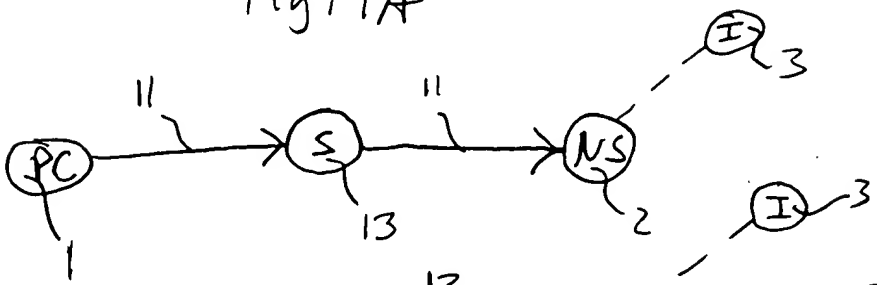


Fig. 4B

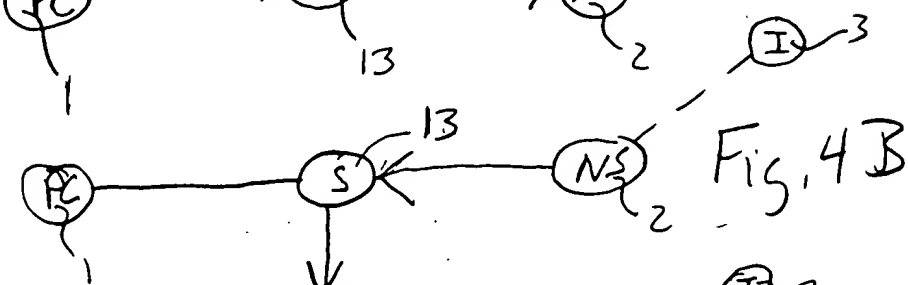


Fig. 4C

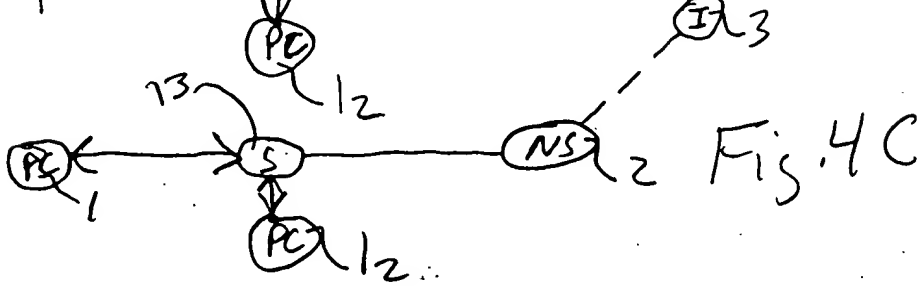


Fig. 5A

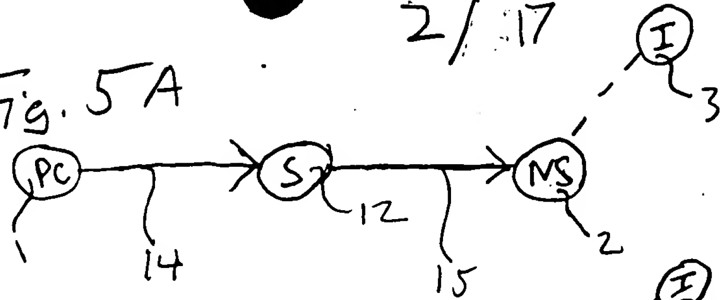


Fig. 5B

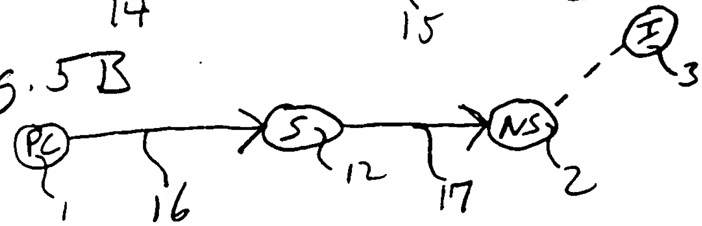


Fig. 6

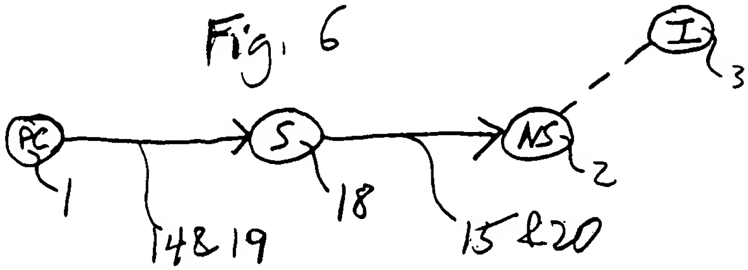


Fig. 7

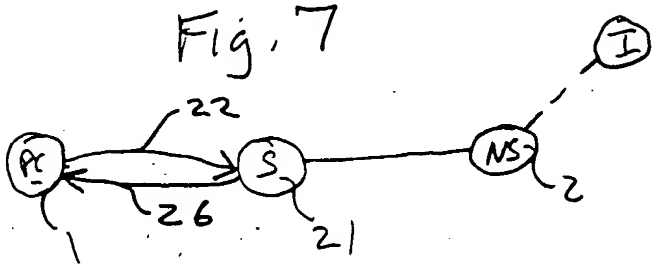


Fig. 8

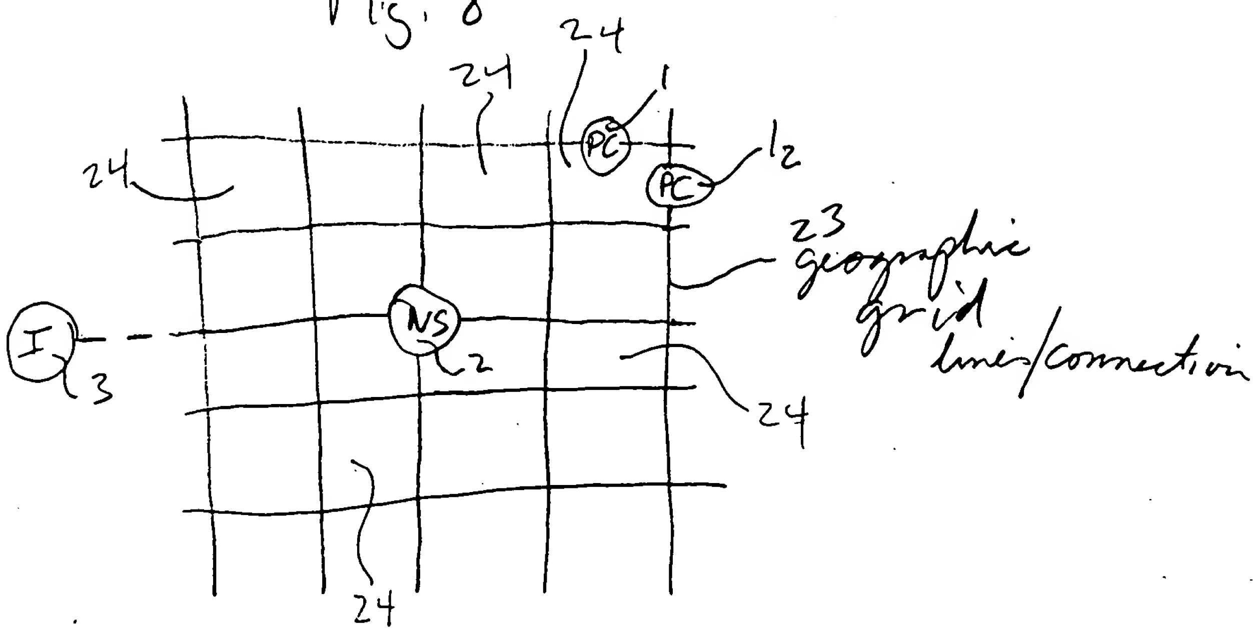


Fig. 10B

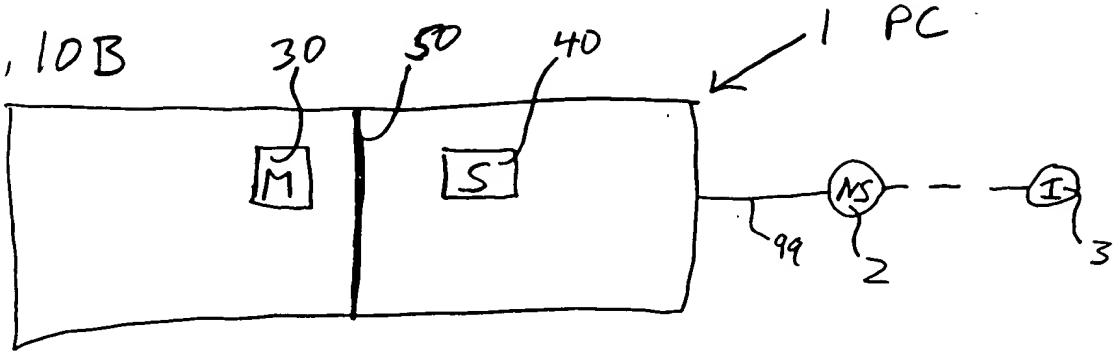
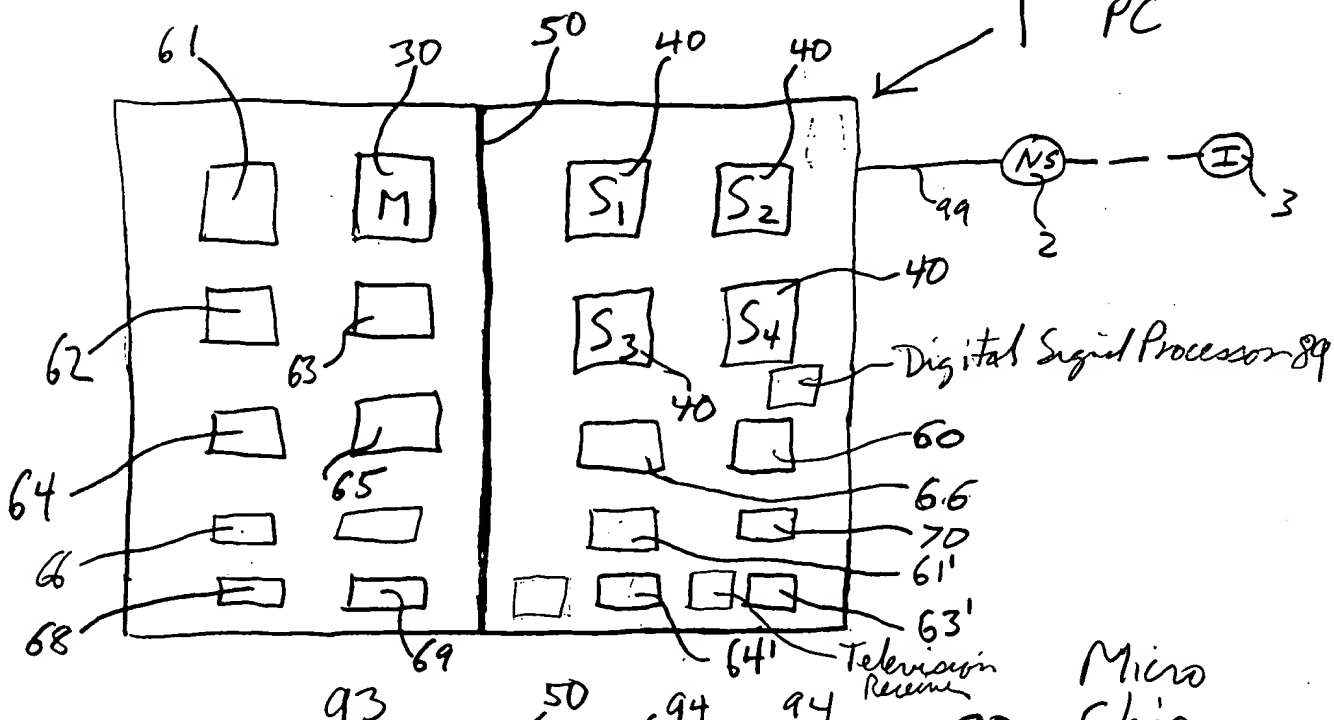


Fig. 10A



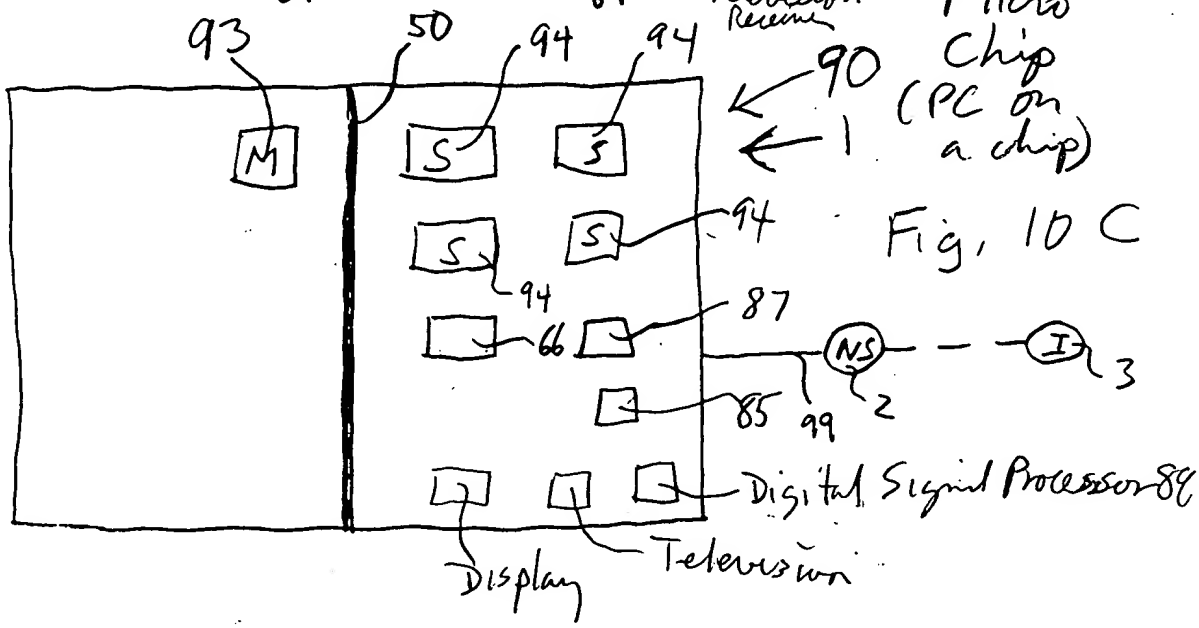


Fig. 10C

Fig. 9

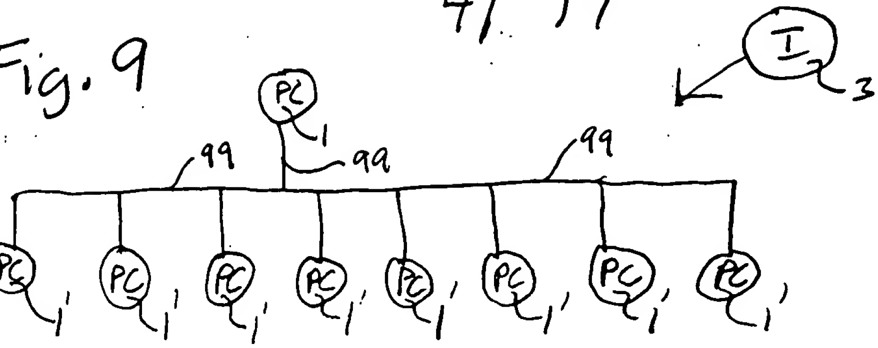


Fig. 10D

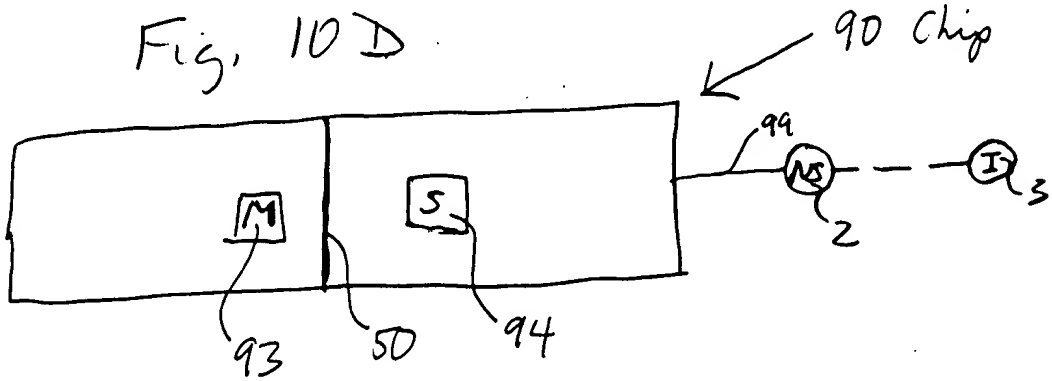


Fig. 11

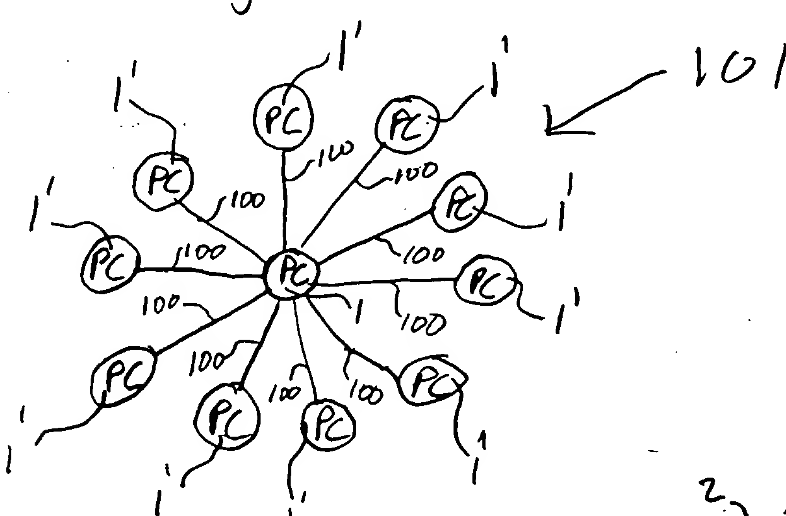


Fig. 10I

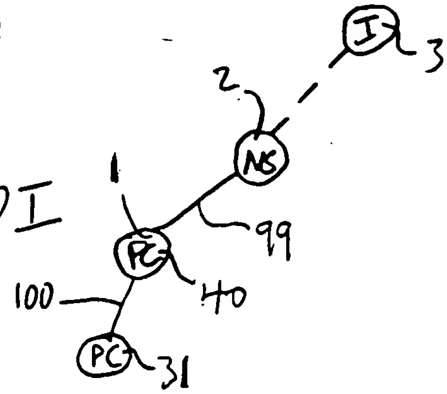


Fig. 10E

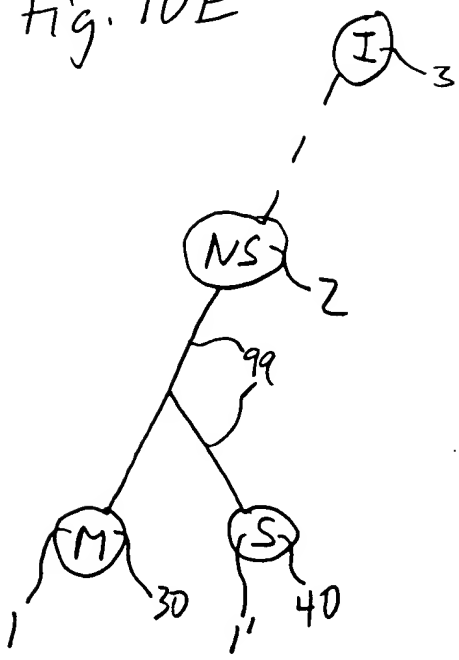


Fig. 10F

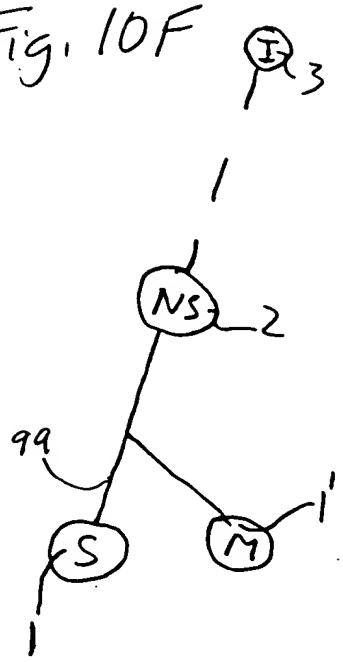


Fig. 10G

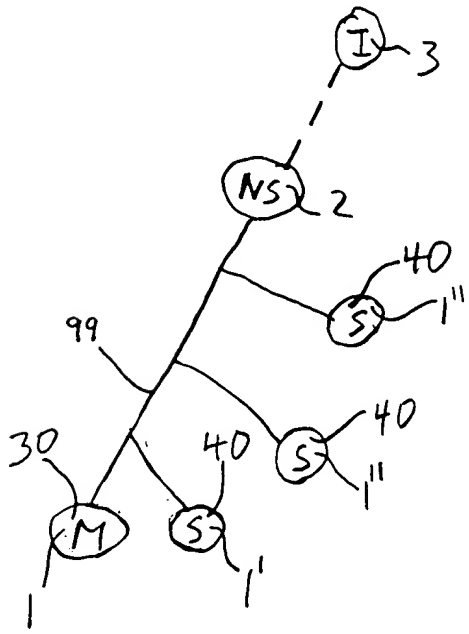


Fig. 10H

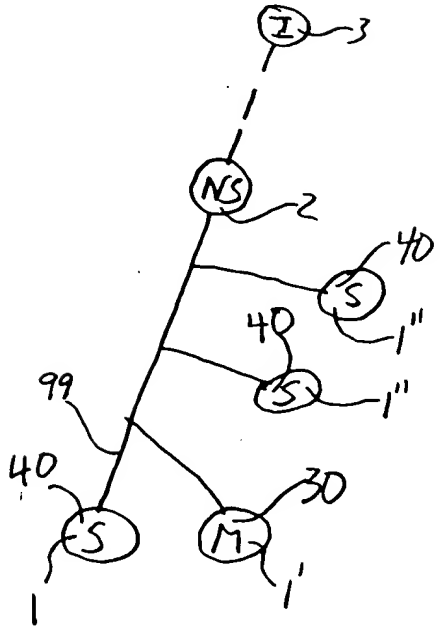


Fig. 12

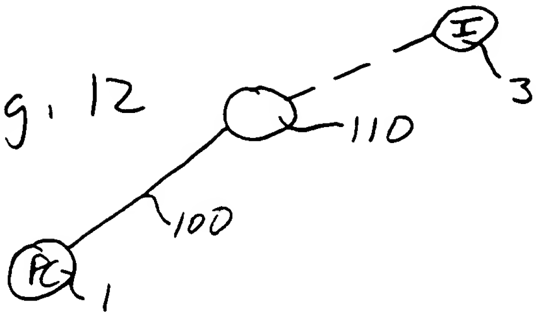


Fig. 14D

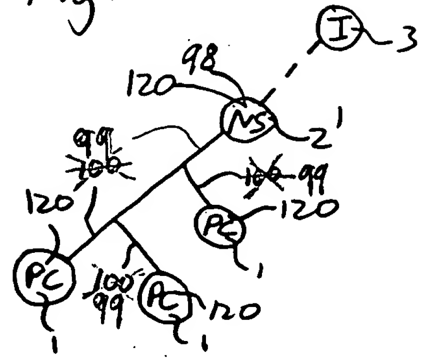


Fig. 13

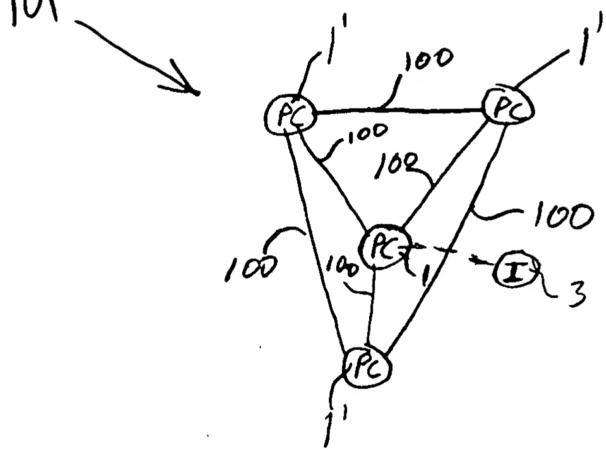


Fig. 14C

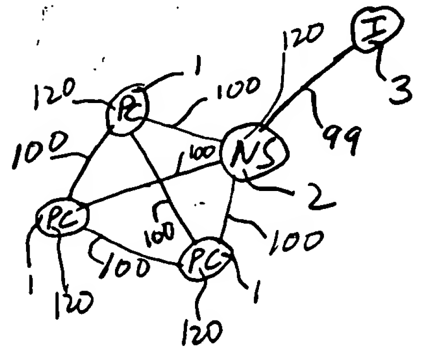
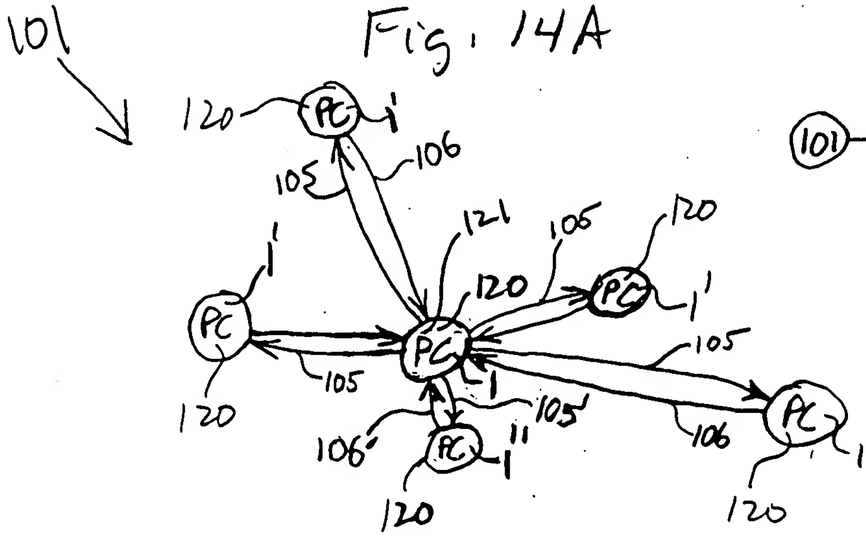


Fig. 14A



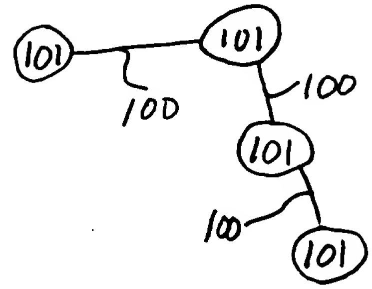


Fig. 14B

Fig. 15

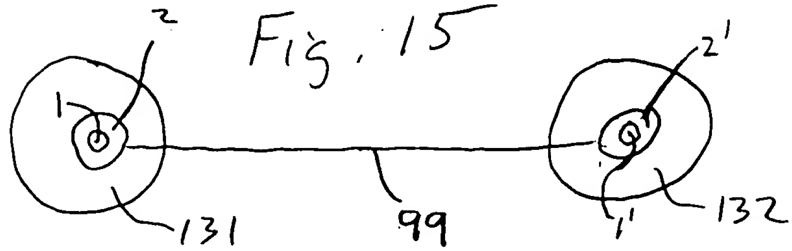


Fig. 10K

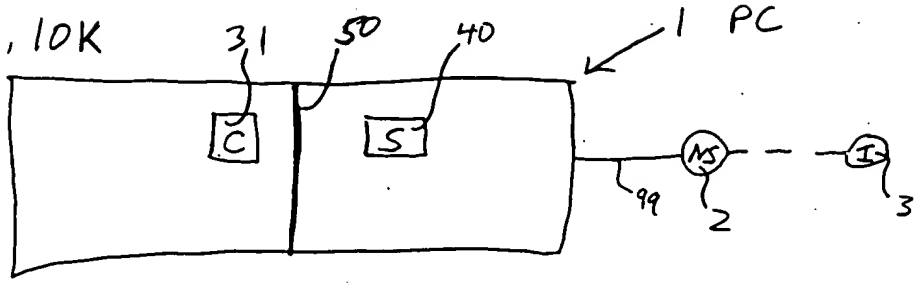
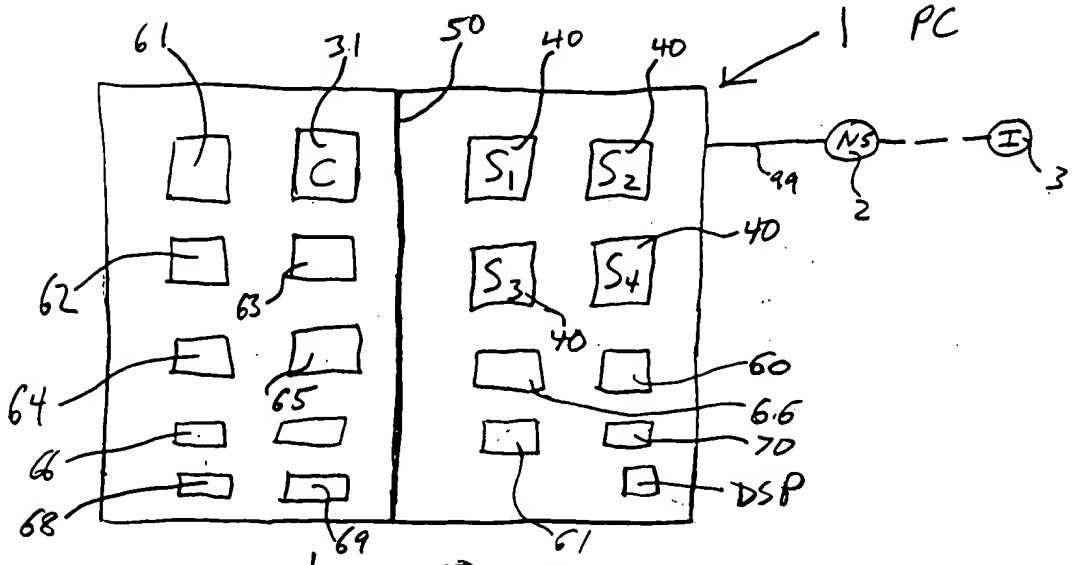


Fig. 10J



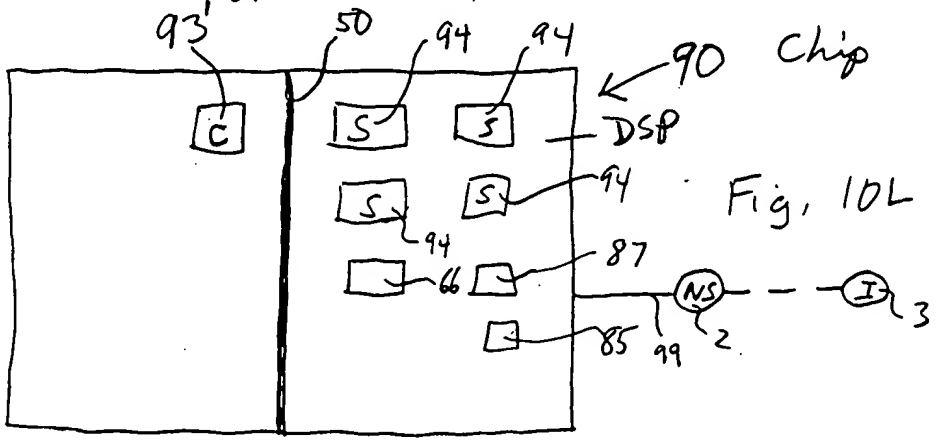


Fig. 10L

Fig. 10M

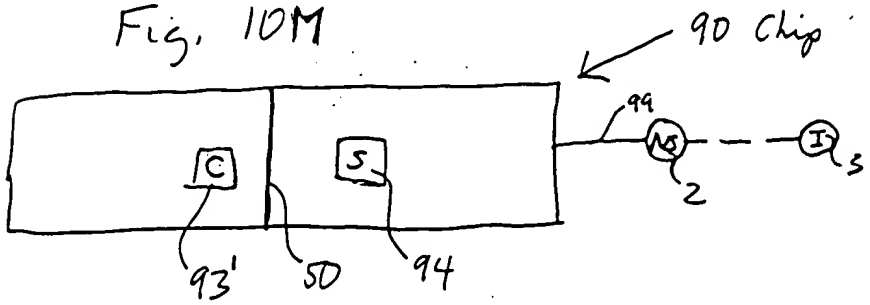


Fig. 10"O"

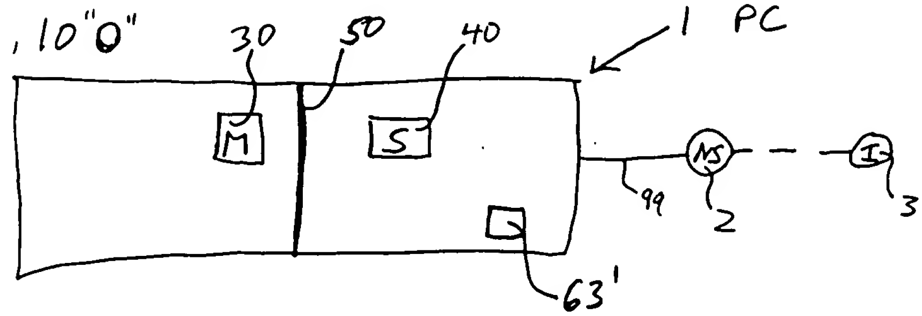
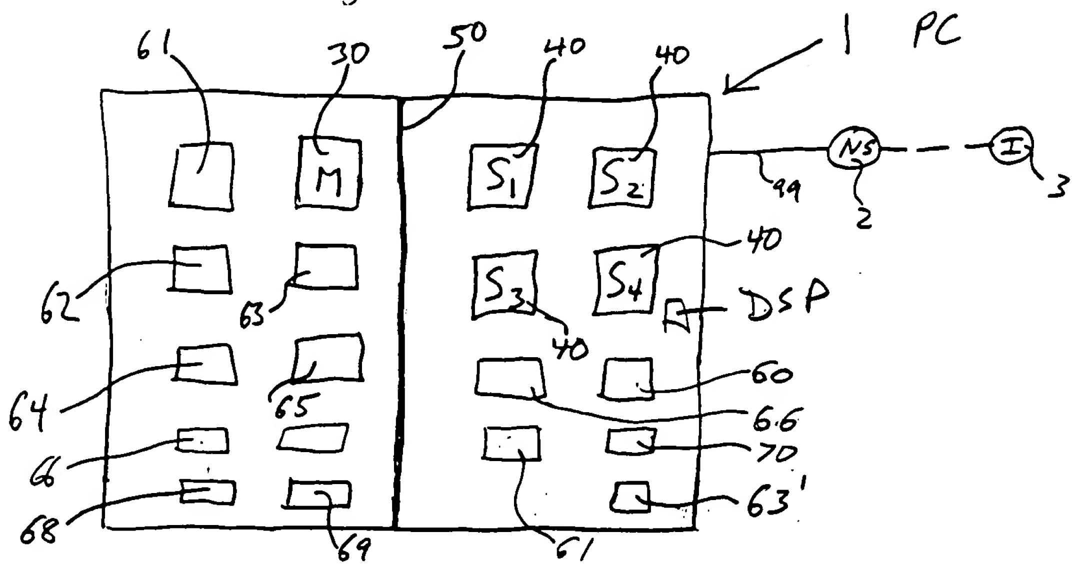


Fig. 10N



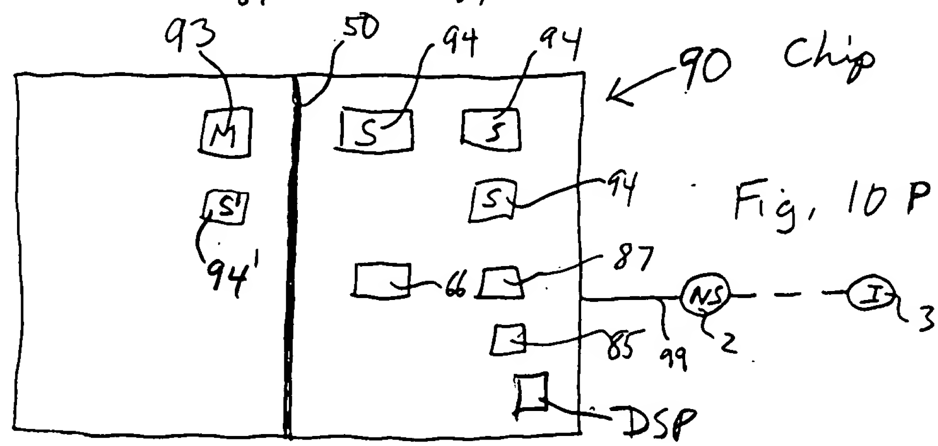


Fig. 10 P

Fig. 10 Q

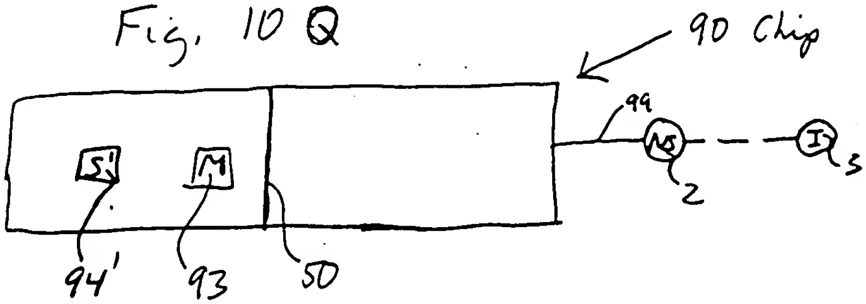
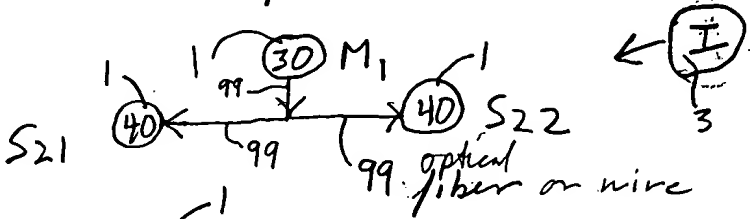
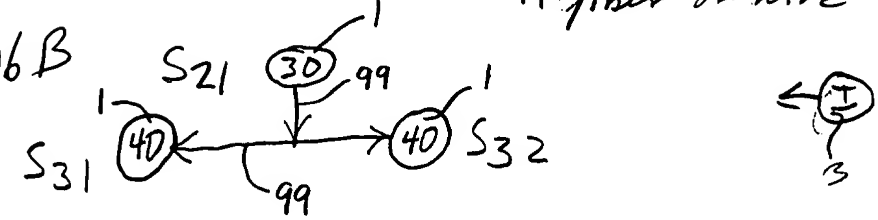


Fig. 16A



(Part of Internet or Intranet or other net)

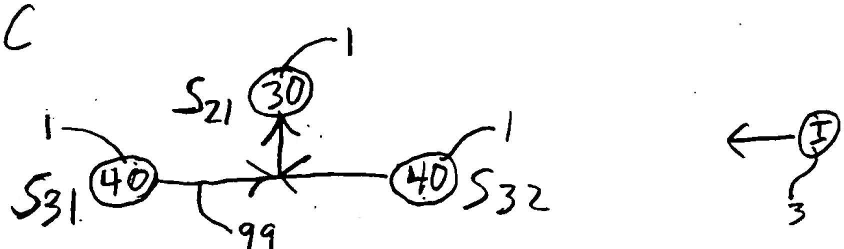
Fig. 16B



Figs 16A–Q & 16V–AA:

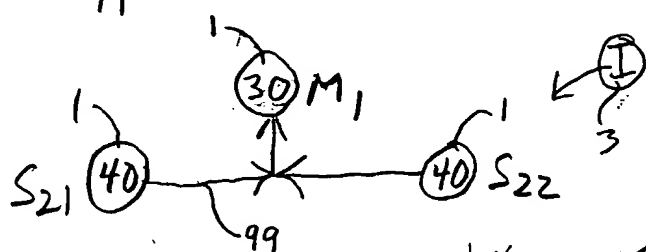
1 (30) indicates either master PC 1 or master microprocessor 30 chip within a PC 1.

Fig. 16C



Likewise, 1 (40) indicates either a slave PC 1 or a slave microprocessor 40 chip within a PC 1.

Fig. 16D



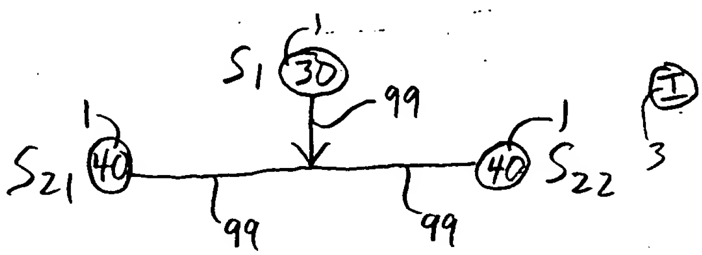
Either microprocessor 30 or microprocessor 40 can be a microprocessor 90, a PC 1 or a microchip

Note 100: mix of 100 & 99

Fig. 16G

Master PC offloads operation to slave PC when functions as M

Fig. 16H



Unavailable S21 offloads results of S31 & S32 to S21' which takes over available

Fig. 16 I

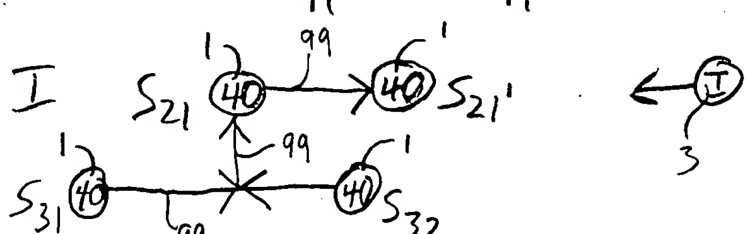
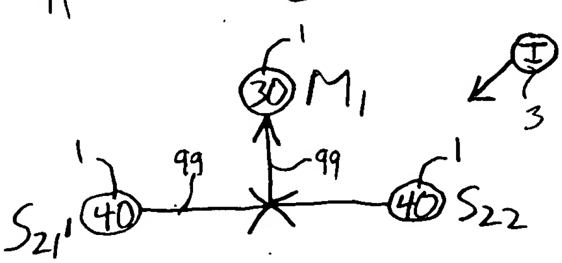


Fig. 16, J



Like Fig. 16D
S21' replaces S21

Fig. 16E

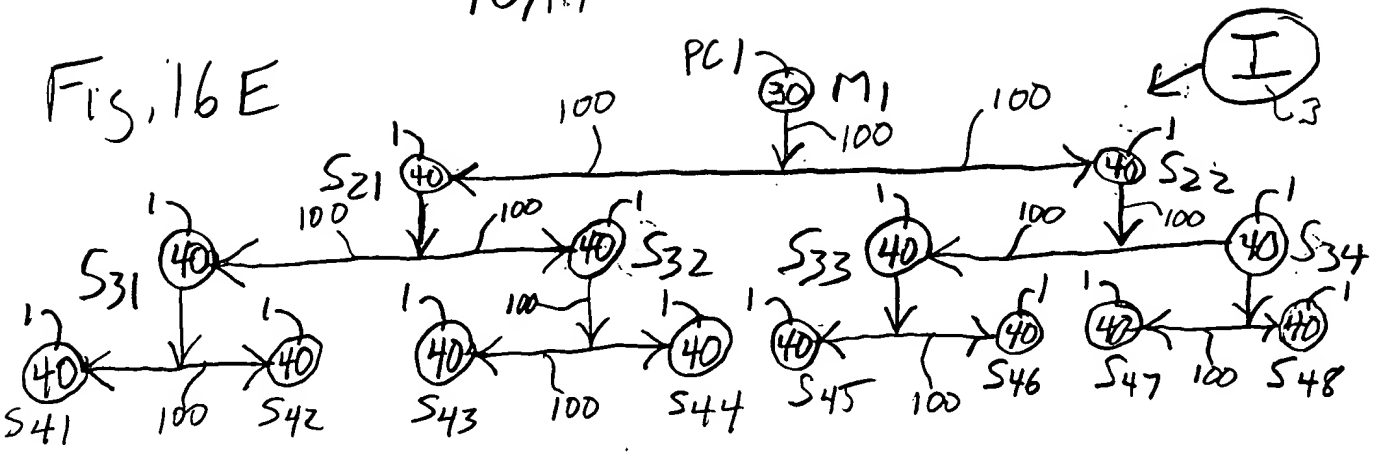


Fig. 16F

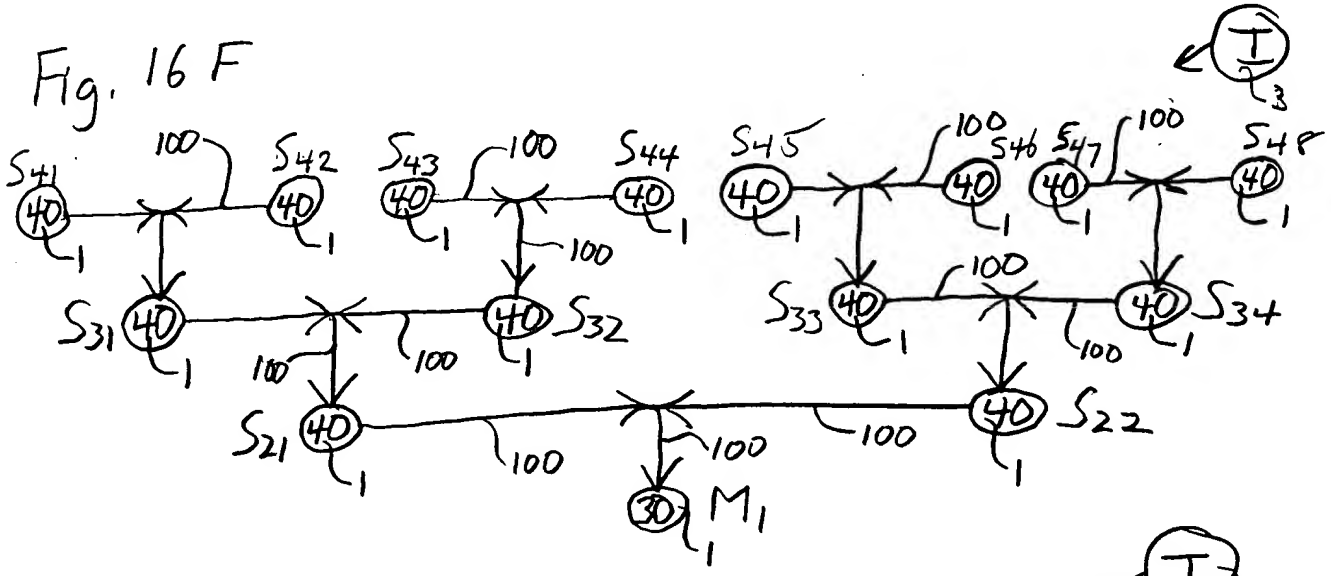


Fig. 16K

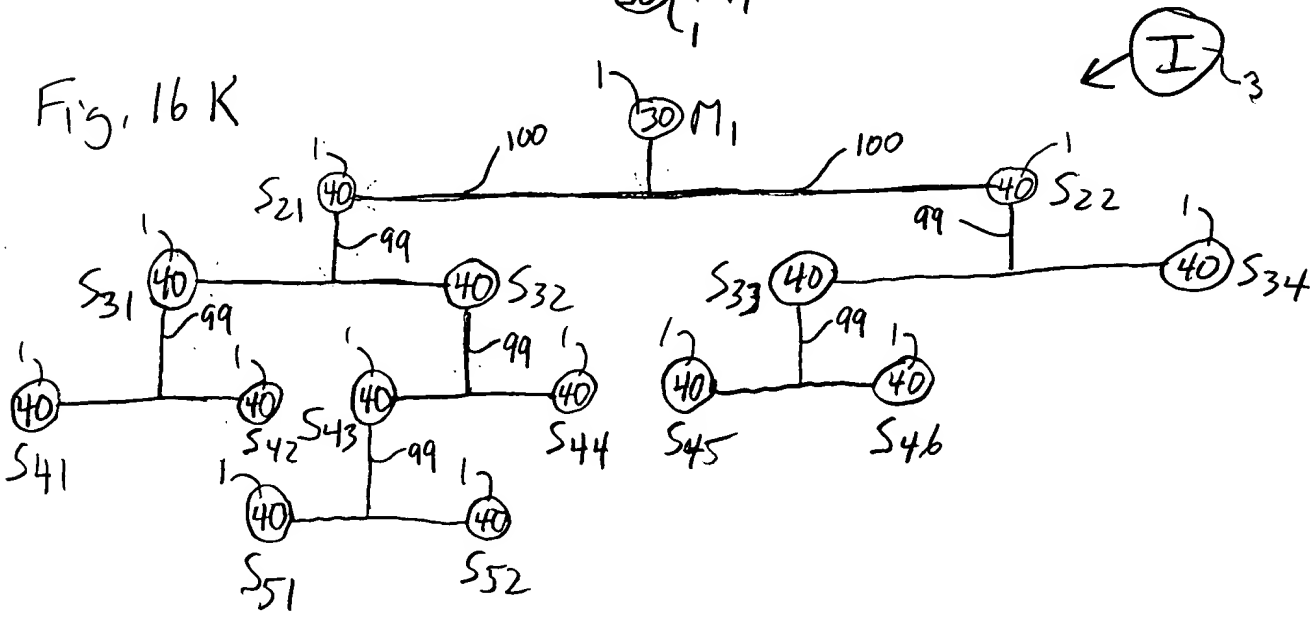


Fig. 16L

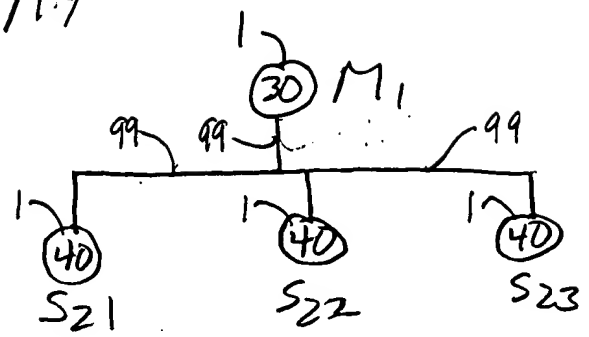


Fig. 16M

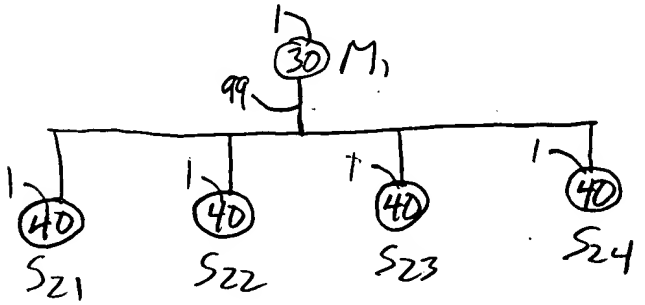


Fig. 16N

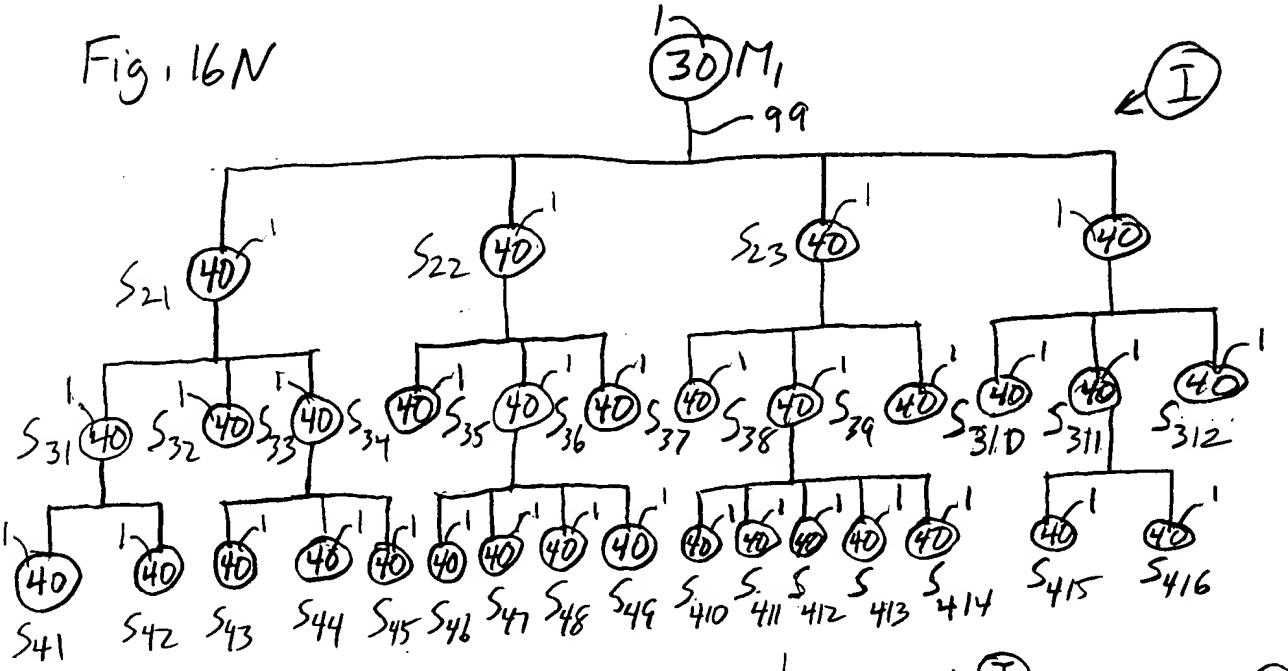


Fig. 16O

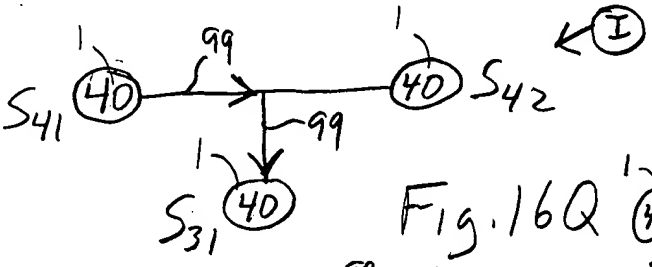


Fig. 16P

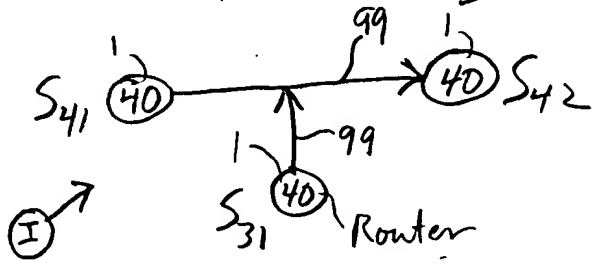
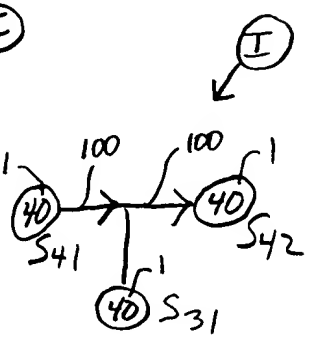


Fig. 16Q



Figs. 16O–Q are sections of Fig 16F Net (left upper)

like Fig 10A & 10B

Fig. 16X

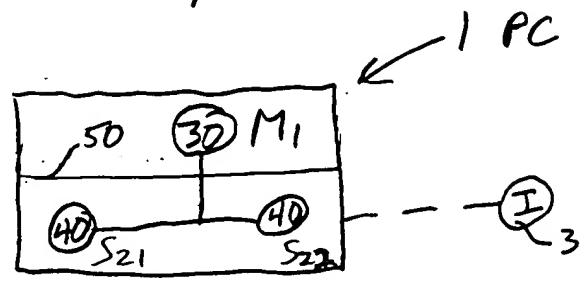


Fig. 16Y

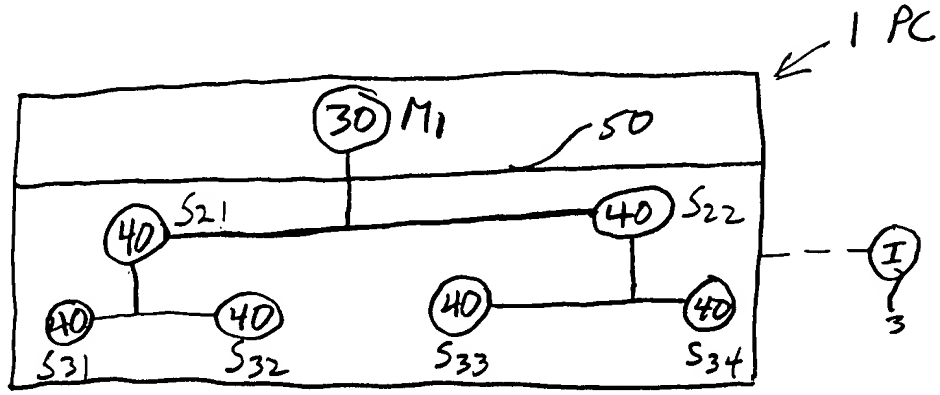


Fig. 16Z

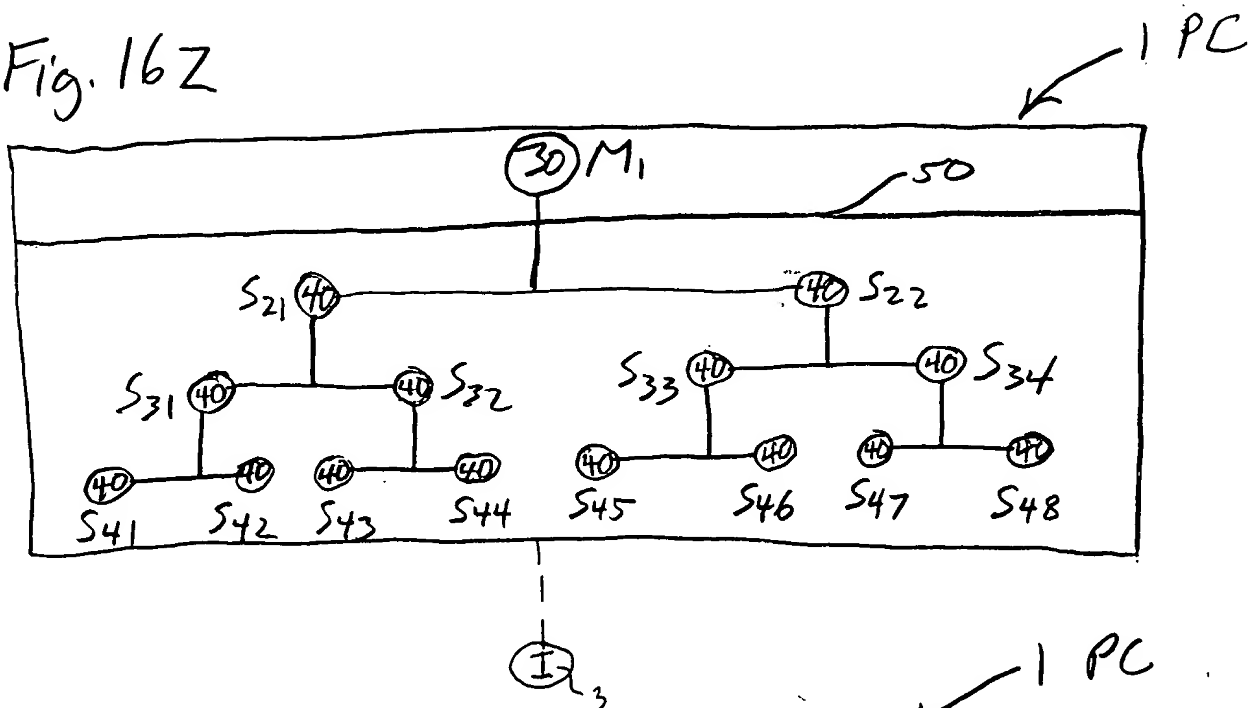
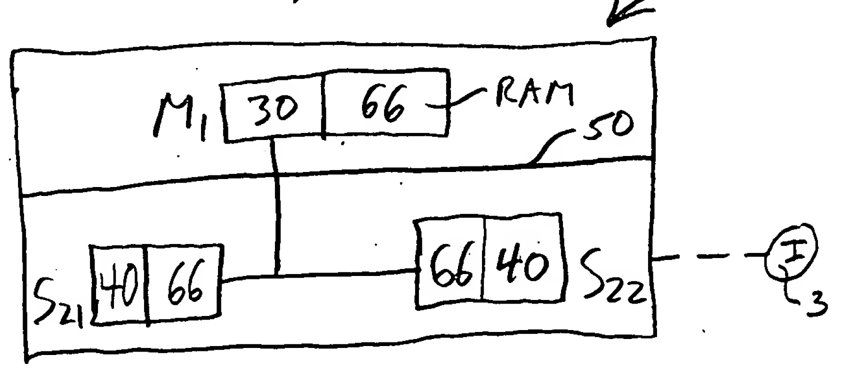


Fig. 16AA



Like Fig. 10C: "Personal Computer on a chip" (Figs. 16R-16U)

Microchip

Fig. 16R

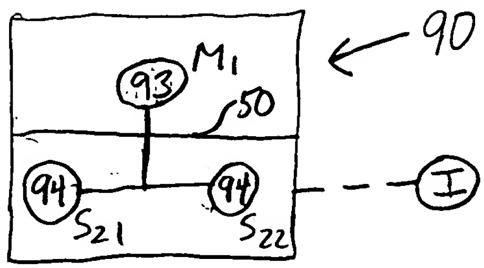


Fig. 16S

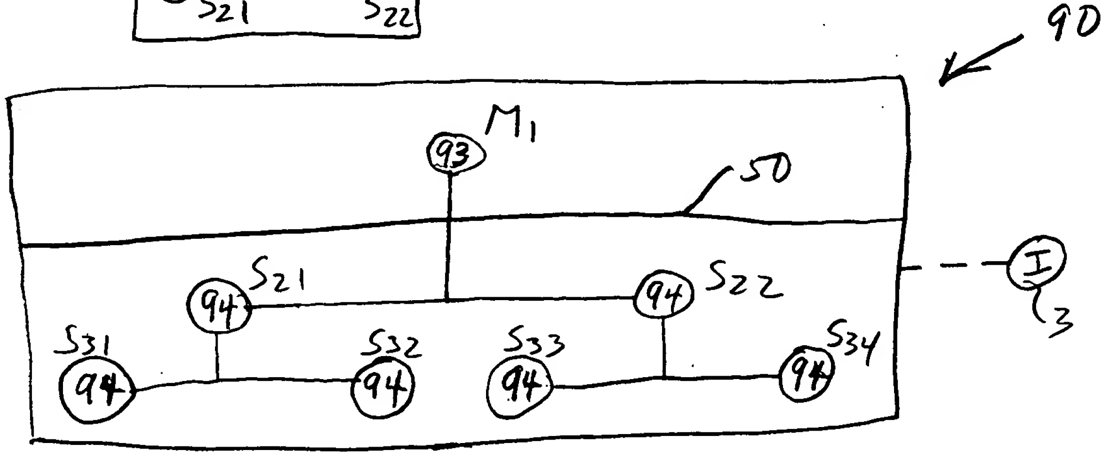


Fig. 16T

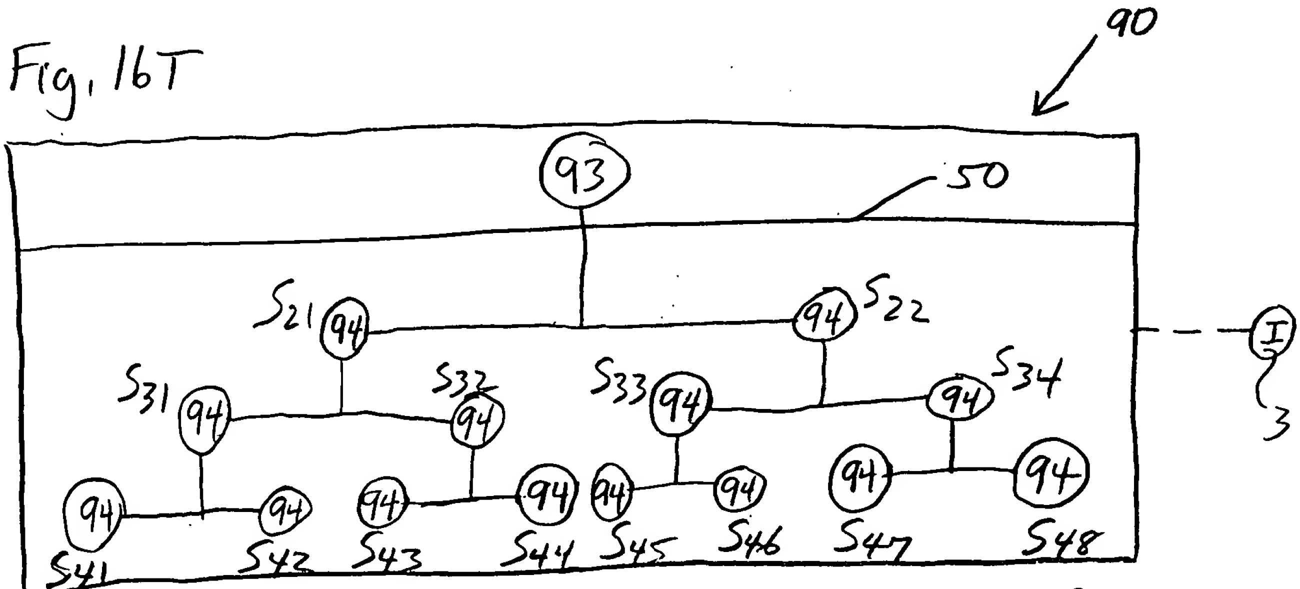
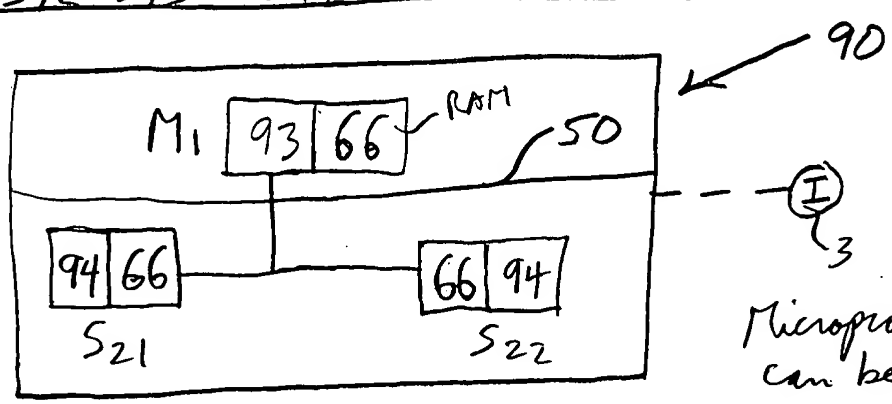


Fig. 16U



Microprocessor 90 can be entire PC1. on a single microchip

Fig. 16W-X follows Fig. 16O-Q & are also sections of Fig. 16F net

Fig. 16V

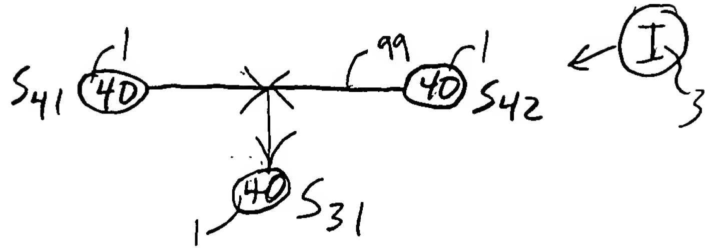


Fig. 16W

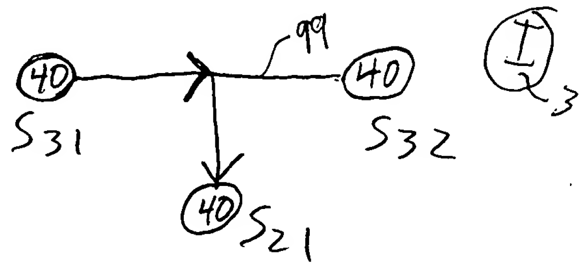


Fig. 17C

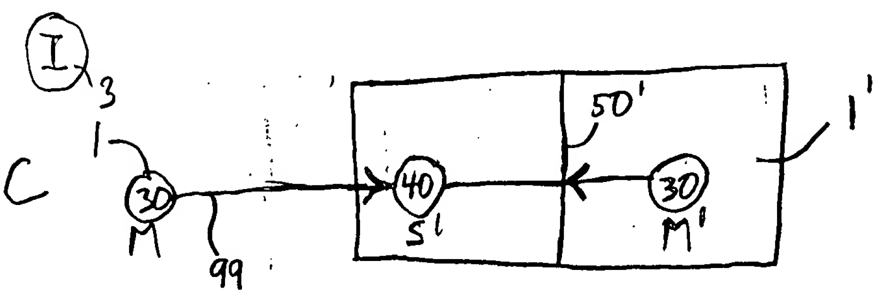


Fig. 17A

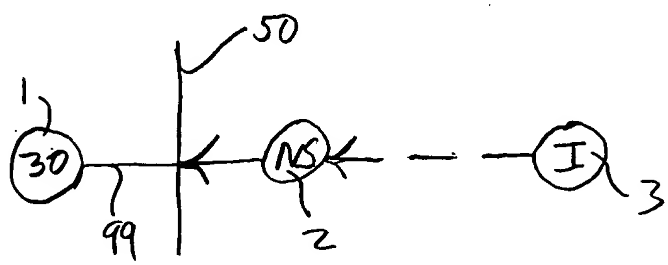


Fig. 17B

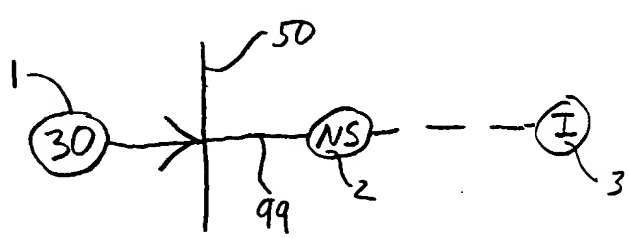


Fig. 17D

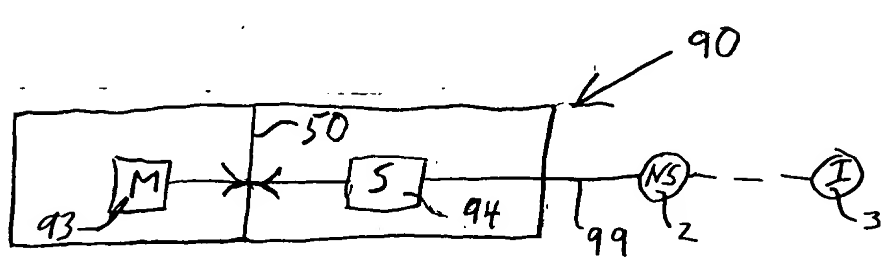


Fig. 18A

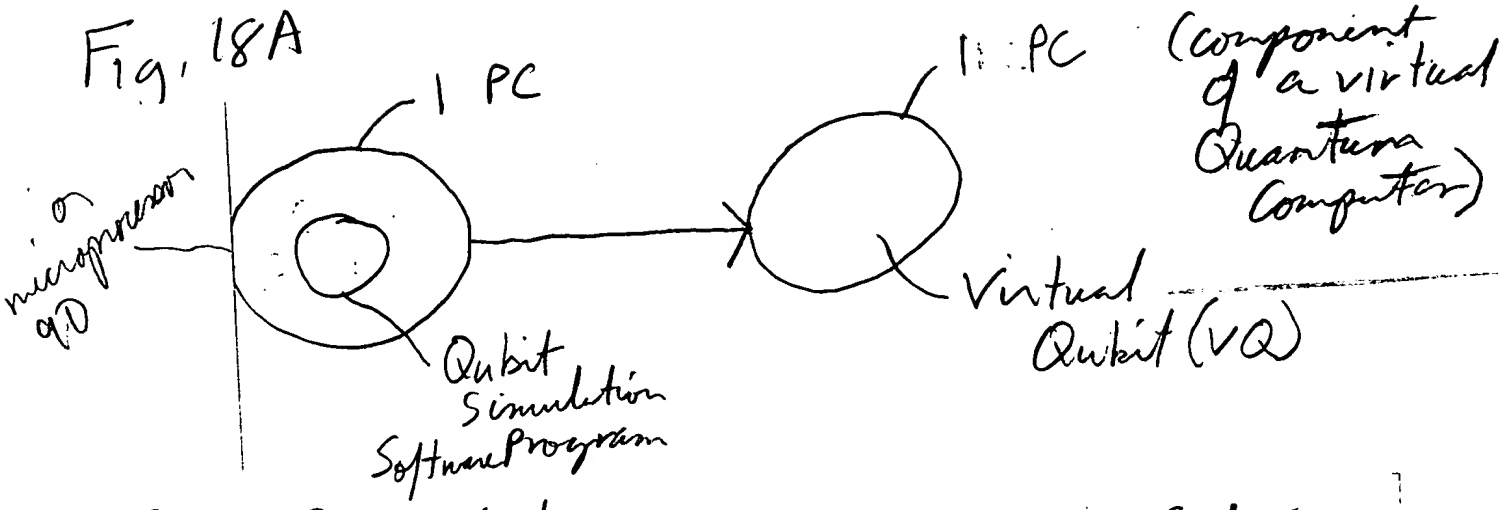


Fig. 18B

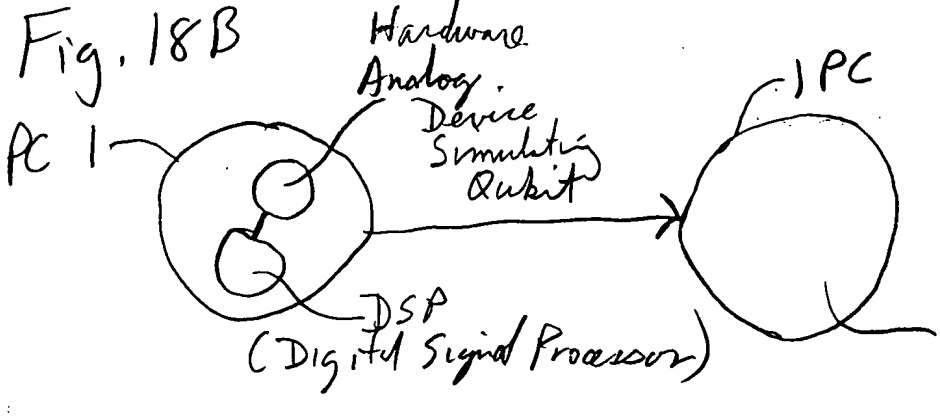


Fig 18B design can allow simultaneously use of PC 1 for both digital & quantum computing

Fig. 18C

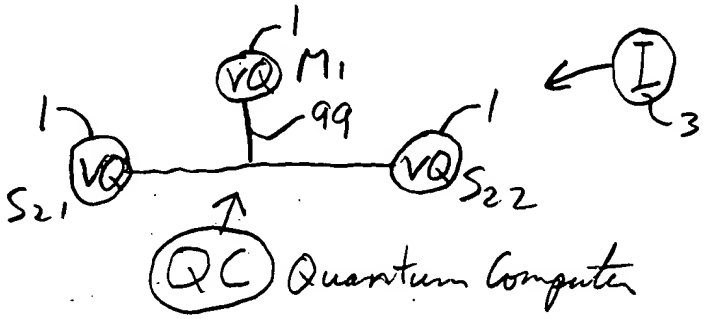
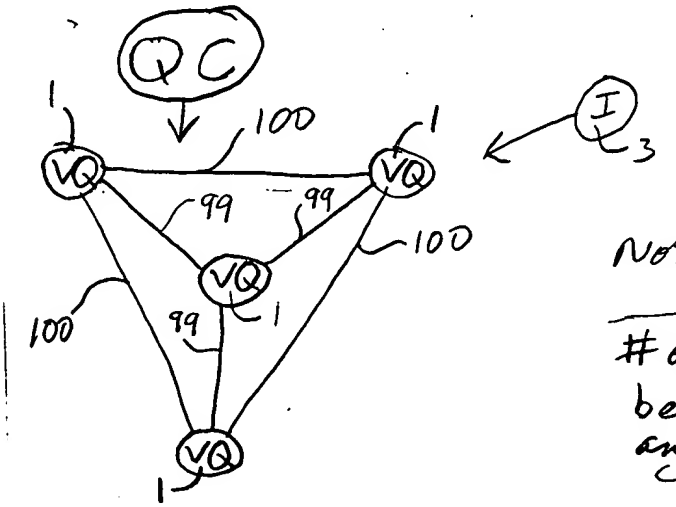


Fig. 18C like Fig. 16A & similarly VQ could be substituted for 30 & 40 in Figs. 16B-16Q & 16V-16AA and in earlier Figures

Fig. 18D



Note 99 & 100 mix

# of VQ can be scaled to any size Quantum Computer QC

Fig. 19

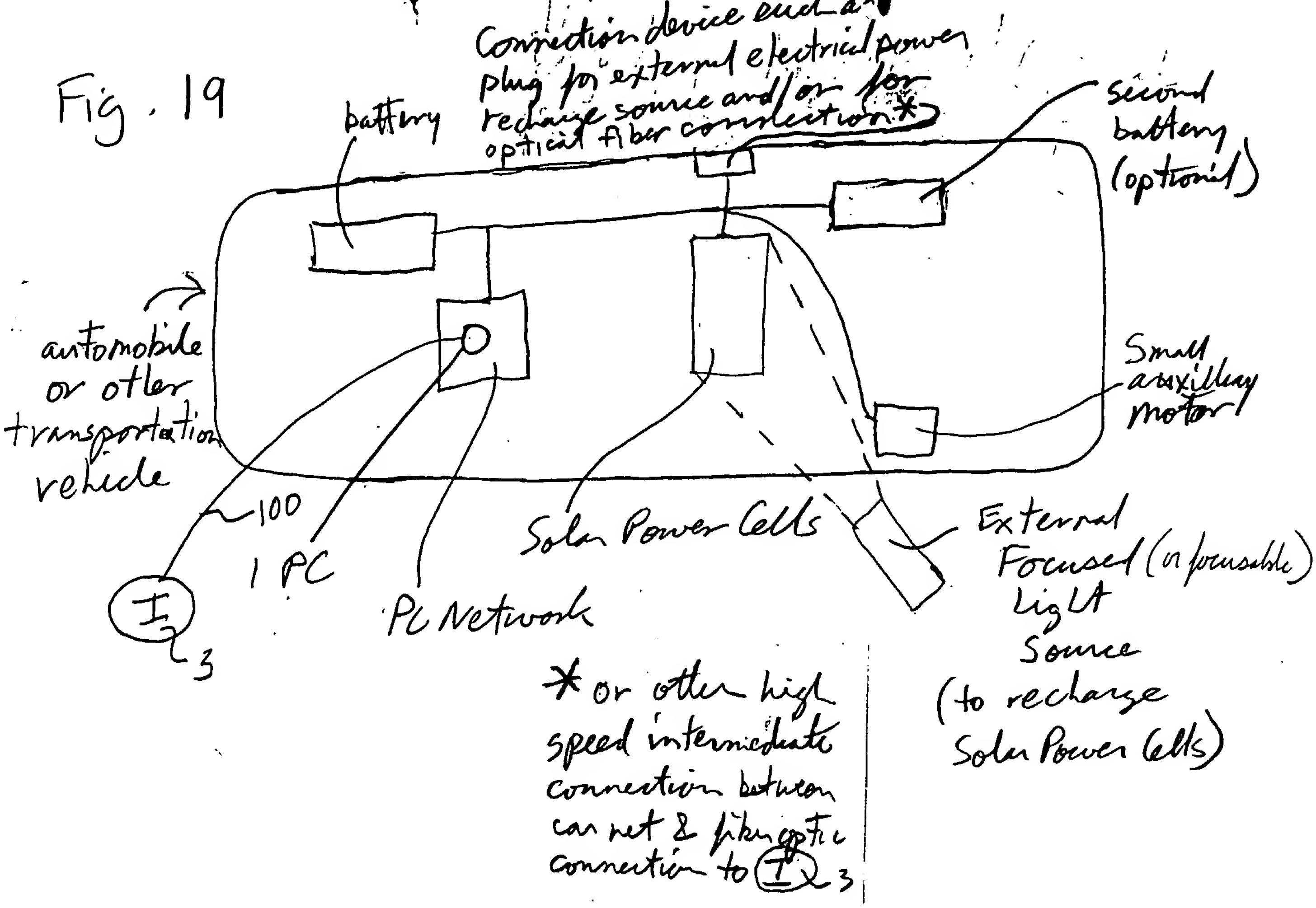

Connection device such as plug for external electrical power recharge source and/or for optical fiber connection*
battery
second battery (optional)
automobile or other transportation vehicle
Small auxillary motor
100
I
3
1 PC
PC Network
Solar Power Cells
External Focused (or focusable) Light Source (to recharge Solar Power Cells)
* or other high speed intermediate connection between car net & fiber optic connection to I 3

Fig. 20A

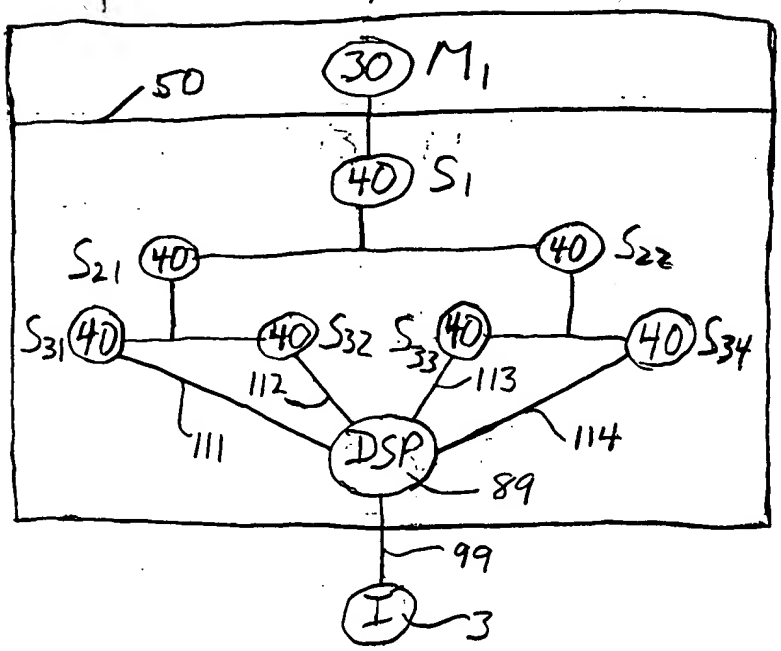


Fig. 20B